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## 1   **6   UMT sublayer**

### 2   **6.1   UMT Classification and Translation Engine**

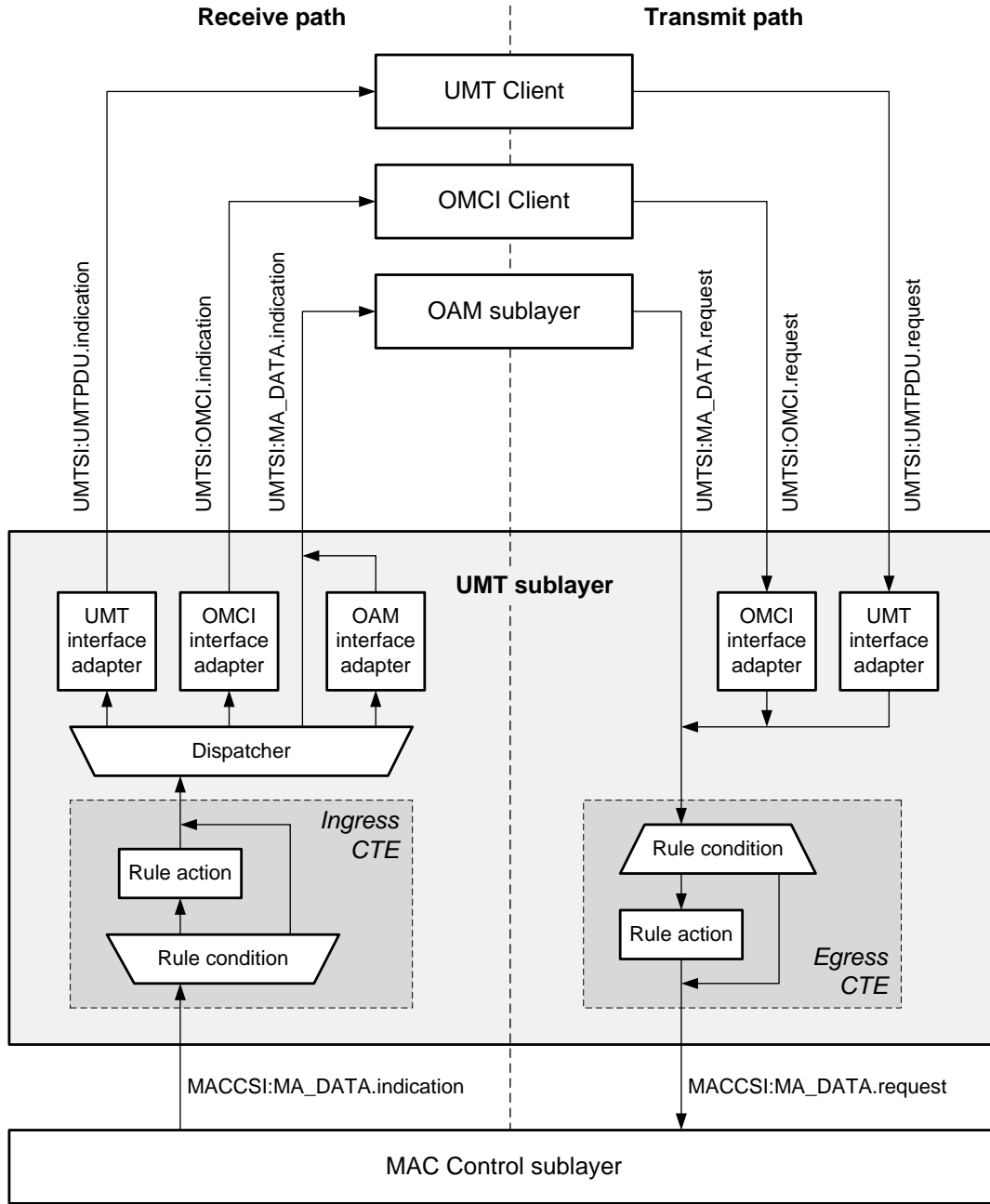
3   The function of the UMT Classification and Translation Engine (CTE) is to classify frames by certain criteria  
4   and to perform specific modification on the frames that match the criteria. The classification criteria together  
5   with the associated modification action comprise an entity called a *rule*. The concept of a rule is similar to  
6   that defined in IEEE 1904.1, 6.5.2.1. By matching frames to specific rules, the CTE is able to translate  
7   UMTPDUs into xPDUs (i.e., into frames with different Ethertype values) and vice versa. ~~A frame that does  
8   not match any CTE rules traverses the UMT sublayer without any modifications.~~

9   There are separate CTE instances in the transmit path and in the receive path of each physical or virtual port.  
10   The CTE located in the receive path is called *Ingress CTE* and the CTE located in the transmit path is called  
11   *Egress CTE* (see Figure 6-1). Fundamentally, a CTE instance is simply a table that stores multiple rules.  
12   Some of the rules are statically pre-configured (i.e., available and active at all times); other rules are  
13   dynamically added/deleted by NMS when tunnels are established or destroyed.

14

15

16   <Replace figure 6-1 with the following figure>



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**Figure 6-1—UMT sublayer functional block diagram**

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**6.1.1 CTE rule structure**

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<as is>

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**6.1.1.1 CTE rule classification conditions**

7

<a is>

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2 **6.1.1.1.1 Comparison operators**

3 &lt;a is&gt;

4

5 **6.1.1.1.2 Classification fields**

6 &lt;a is&gt;

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8 **6.1.1.2 CTE rule modification actions**

9 &lt;a is&gt;

10

11 **6.1.2 CTE rule categories**

12 &lt;a is&gt;

13

14 **6.1.3 CTE rules involving operations on the VLAN tags**

15 The classification clauses in the CTE rules may classify the incoming xPDUs and UMTPDUs based on  
16 *VLAN0* or *VLAN1* fields, or based on some sub-fields of these fields (see Table 6-2).

17 The action clauses in the CTE rules may add *VLAN0* and *VLAN1* tags to UMTPDUs or delete these tags  
18 from UMTPDUs. When performing a translation of an xPDU into a UMTPDU, and if the original xPDU  
19 includes any VLAN tags, the action clauses may also copy these tags from xPDU into UMTPDU. The COPY  
20 operation leaves the VLAN tags in the original xPDU intact.

21 Even though the UMT sublayer may be configured to manipulate VLAN tags in UMTPDUs, it does not  
22 imply that a given UMT-aware device is also VLAN-aware and that it is a participant in Multiple VLAN  
23 Registration Protocol (MVRP). The VLAN manipulation applied by the UMT sublayer is entirely based on  
24 the provisioned CTE rules and not on any higher-layer protocol behavior or device configuration. In a VLAN-  
25 enabled L2 network, the management entity responsible for UMT port configuration and provisioning is  
26 expected to be aware of VLAN topology and to participate in MVRP if necessary.

27 **6.2 Receive path specification**28 **6.2.1 Principles of operation**

29 The receive path of the UMT sublayer includes the Receive process. The Receive process waits for a frame  
30 to be received on MACCSI:MA\_DATA interface (via `MACCSI:MA_DATA.request()` primitive, as  
31 defined in 4.4). When a frame is received, it is processed by the ingress Classification and Translation Engine  
32 (CTE) and if match is found, the frame is modified according to the matched rule action. If the frame does  
33 not match any rules, it is passed through the CTE block unmodified.

34 After traversing the ingress CTE block (highlighted in Figure 6-4), the frame is dispatched to one of the  
35 UMTSI interfaces: (UMTSI:UMTPDU, UMTSI:OMCI, or UMTSI:MA\_DATA). The dispatching decision  
36 is based on the values of the MAC destination address, Ethertype, and UMT subtype.

1 The UMTPDUs with the destination address matching the local MAC address and the UMT subtype equal  
 2 to UMT\_SUBTYPE (see Table 5.1) are modified to match the parameters expected by the UMTSI:UMTPDU.  
 3 indication() primitive (see 4.4.x) and are passed to the UMTSI:UMTPDU interface.

4 The UMTPDUs with the destination address matching the local MAC address and the UMT subtype equal  
 5 to OAM\_SUBTYPE (see Table 5.1) are converted into OAMPDUs and are passed to the UMTSI:MA\_DATA  
 6 interface.

7 The UMTPDUs with the destination address matching the local MAC address and the UMT subtype equal  
 8 to OMCI\_SUBTYPE (see Table 5.1) are modified to match the parameters expected by the UMTSI:OMCI.  
 9 indication() primitive (see 4.4.y) and are passed to the UMTSI:OMCI interface.

10 All other xPDUs are passed unmodified to the UMTSI:MA\_DATA interface. Note that there still may be  
 11 other local clients that will intercept/consume these xPDUs at a higher layer.

12 The Receive process does not discard any frames, i.e., every MACCSI:MA\_DATA.indication()  
 13 primitive results in a generation of a single indication primitive on either UMTSI:UMTPDU, UMTSI:OMCI,  
 14 or UMTSI:MA\_DATA interface.

15 Note that no provisioning of the ingress tunnel exit rules is required in situations where the tunnel is  
 16 terminated at the same port where the xPDUs are to be consumed by their respective clients. The functionality  
 17 to convert UMTPDUs into xPDUs is built-in into the Receive process.

## 18 6.2.2 Constants

19 DST\_ADDR

20 This constant identifies a field in a frame, as defined in Table 6.1.

21 ETH\_TYPE\_LEN

22 This constant identifies a field in a frame, as defined in Table 6.1.

23 LOCAL\_MAC\_ADDR

24 TYPE: 48-bit MAC address

25 This constant holds the value of the MAC address associated with the port where the Receive  
 26 process state diagram is instantiated. Some devices may associate the same MAC address value with  
 27 multiple ports. The format of MAC address is defined in IEEE Std 802.3, 3.2.3.

28 VALUE: device-specific

29 OMCI\_SUBTYPE

30 This constant represents a UMTPDU subtype as defined in Table 5.1.

31 SP\_ADDR

32 This constant holds the value of the destination MAC address associated with Slow Protocols (see  
 33 IEEE Std 802.3, 57A.3).

34 SP\_TYPE

35 This constant holds the value of the Ethertype identifying the Slow Protocol (see IEEE Std 802.3,  
 36 57A.4).

1 SRC\_ADDR

2 This constant identifies a field in a frame, as defined in Table 6.1.

3 SUBTYPE

4 This constant identifies a field in a frame, as defined in Table 6.1.

5 UMT\_ETHERTYPE

6 TYPE: 16-bit Ethertype

7 This constant holds the Ethertype value identifying the UMTPDUs.

8 VALUE: 0xA8-C8

9 UMT\_SUBTYPE

10 This constant represents a UMLTPDU subtype as defined in Table 5.1.

11

### 12 6.2.3 Variables

13 IngressRuleId

14 TYPE: 16-bit unsigned integer

15 This variable identifies one of the provisioned CTE ingress rules. It also may have a special value  
16 none that does not identify any of the provisioned rules.

17 RxInputPdu

18 TYPE: structure containing an Ethernet frame

19 This variable holds an Ethernet frame received from the MACCSI:MA\_DATA interface. The fields  
20 of this structure correspond to the parameters of the MA\_DATA.indication() primitive, as  
21 defined in IEEE Std 802.3, 2.3.2.

22 RxOutputPdu

23 TYPE: structure containing an Ethernet frame

24 This variable holds an Ethernet frame to be passed to one of the the UMTSI interfaces  
25 (UMTSI:UMTPDU, UMTSI:OMCI, or UMTSI:MA\_DATA). The fields of this structure  
26 correspond to the parameters of the MA\_DATA.indication() primitive, as defined in IEEE Std  
27 802.3, 2.3.2.

28 Additionally, the RxOutputPdu structure supports the RemoveField(field\_code) method,  
29 which removes a field identified by the field\_code from the structure. Thus, unlike the  
30 RxInputPdu structure, the RxOutputPdu may contain only a partial Ethernet frame. The  
31 field\_code parameter takes values as defined in Table 6.1.

### 32 6.2.4 Functions

33 CheckIngressRules(input\_pdu)

34 This function returns the identification of an ingress rule that matched the frame contained in  
35 RxInputPdu structure. If multiple rules machted the frame, the function returns an identification  
36 of any of these rules. If none of the rules matched the frame, a special value none is returned.

1 `Modify(rule_id, input_pdu)`

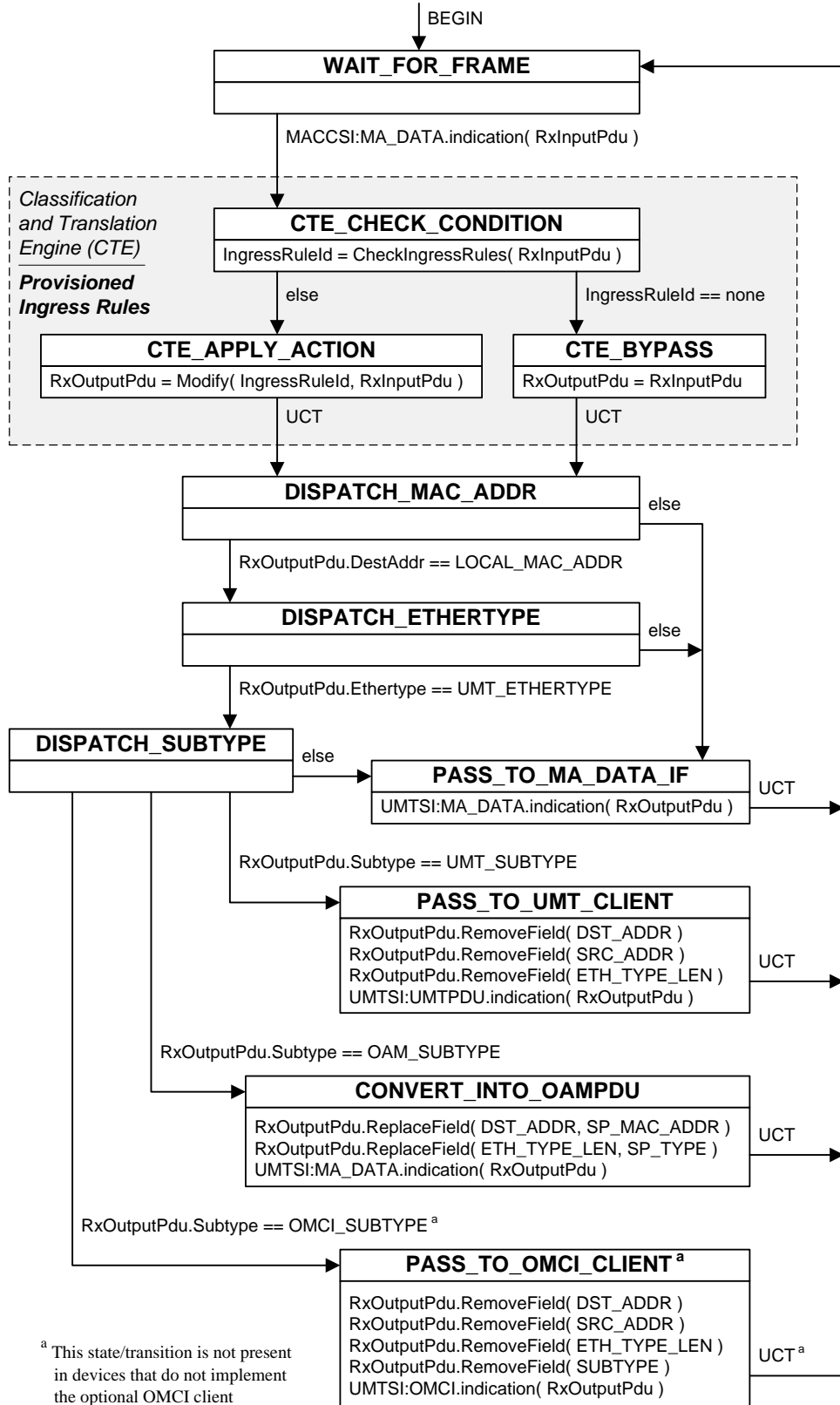
2         This functions returns a frame that is a result of applying the modification action(s) of the rule  
3         identified by the `rule_id` parameter to the frame contained in the `input_pdu` parameter.

#### 4 **6.2.5 Primitives**

5 The primitives referenced in this state diagram are defined in 4.4.

#### 6 **6.2.6 State Diagram**

7 UMT sublayer shall implement the Receive process as defined in the state diagram in Figure 6-4.



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Figure 6-4—Receive process state diagram



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## 2 **6.3 Transmit path specification**

### 3 **6.3.1 Principles of operation**

4 The transmit path of the UMT sublayer includes the Transmit process. The Transmit process waits for an  
5 xPDU to be received from one of the UMTSI interfaces: (UMTSI:MA\_DATA, UMTSI:UMTPDU, or  
6 UMTSI:OMCI).

7 If an UMT xPDU is received from the UMTSI:UMTPDU interface, it is converted into UMTPDU with  
8 subtype UMT\_CONFIG (see Table 5.1) by prepending a UMTPDU header to the UMT xPDU payload. The  
9 header consists of the destination address, source address, and Ethertype fields. Note that both the  
10 destination and the source addresses are equal to the local MAC address assigned to the given port.

11 If an OMCI xPDU is received from the UMTSI:OMCI interface, it is converted into UMTPDU with subtype  
12 OMCI\_SUBTYPE (see Table 5.1) by prepending a UMTPDU header to the UMT xPDU payload. The header  
13 consists of the destination address, source address, Ethertype, and subtype fields. Note that both the  
14 destination and the source addresses are equal to the local MAC address assigned to the given port.

15 After the above modifications, the UMT or OMCI xPDU is formed into a complete frame, which is then  
16 processed by the Egress Classification and Translation Engine (CTE). If match is found, the frame is modified  
17 according to the matched rule action. If the frame does not match any rules, it is passed through the CTE  
18 block unmodified.

19 Note that to enter a tunnel, the UMT xPDU or the OMCI xPDU require a matching egress CTE rule that, as  
20 a minimum, overwrites the local MAC address value in the UMTPDU destination address field with the  
21 MAC address associated with the xPDU destination for the given tunnel.

### 22 **6.3.2 Constants**

23 The constants referenced in this state diagram are defined in 6.2.2.

### 24 **6.3.3 Variables**

25 EgressRuleId

26 TYPE: 16-bit unsigned integer

27 This variable identifies one of the provisioned CTE egress rules. It also may have a special value  
28 none that does not identify any of the provisioned rules.

29 TxInputPdu

30 TYPE: structure containing an Ethernet frame

31 This variable holds a PDU received from one of the the UMTSI interfaces (UMTSI:UMTPDU,  
32 UMTSI:OMCI, or UMTSI:MA\_DATA). When received from the UMTSI:MA\_DATA interface,  
33 the TxInputPdu structure contains a complete and properly-formed Ethernet frame. When  
34 received from UMTSI:UMTPDU or UMTSI:OMCI interfaces, the TxInputPdu structure  
35 contains a partial frame, that only includes the parameters defined for the respective request()  
36 primitive (see 4.4).

37 Additionally, the TxInputPdu structure supports the AddField(field\_code,  
38 field\_value) method, which adds a field identified by the field\_code and having the value  
39 field\_value to the structure. The field\_code parameter takes values as defined in Table  
40 6.1.

1 TxOutputPdu

2       TYPE: structure containing an Ethernet frame

3       This variable holds an Ethernet frame to be passed to the MACCSI:MA\_DATA interface. The fields  
4       of this structure correspond to the parameters of the MA\_DATA.request ( ) primitive, as defined  
5       in IEEE Std 802.3, 2.3.1. A CTE egress rule is considered misconfigured if applying this rule to the  
6       TxInputPdu results in a malformed Ethernet frame being stored in the TxOutputPdu structure.

#### 7 **6.3.4 Functions**

8 CheckEgressRules(input\_pdu)

9       This function returns the identification of an egress rule that matched the the frame contained in  
10       TxInputPdu structure. If multiple rules mached the frame, the function returns an identification  
11       of any of these rules. If none of the rules matched the frame, a special value none is returned.

12 Modify(rule\_id, input\_pdu)

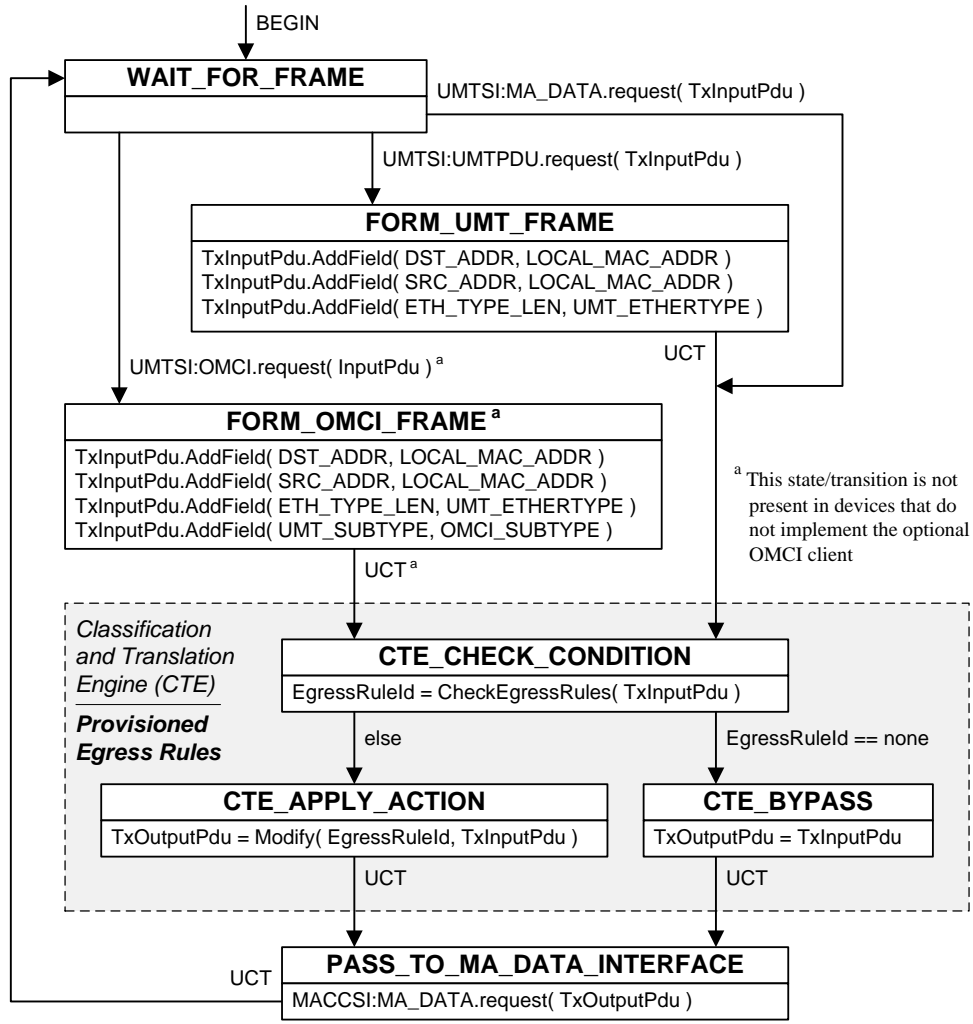
13       This functions is defined in 6.2.4.

#### 14 **6.3.5 Primitives**

15       The primitives referenced in this state diagram are defined in 4.4.

#### 16 **6.3.6 State Diagram**

17       UMT sublayer shall implement the Transmit process as defined in the state diagram in Figure 6-5.



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Figure 6-5—Transmit process state diagram